SSD1963 CONTROLLER MODULE

SSD1963 Controller Module is a display controller of 1215K byte frame buffer to support up to 864 x 480 x 18bit graphics content. It also equips parallel MCU interfaces in different bus width to receive graphics data and command from MCU. Its display interface supports common RAM-less LCD driver of color depth up to 18 bit-perpixel.

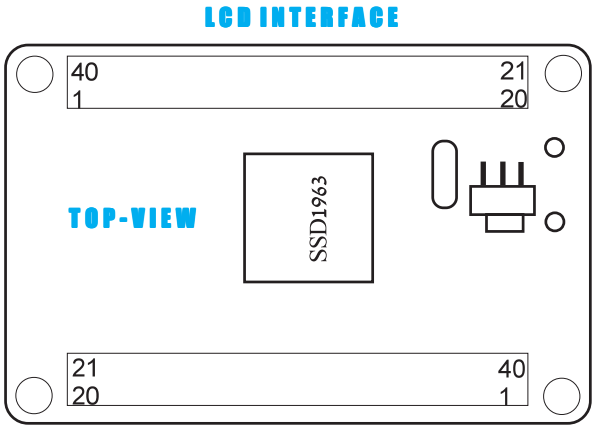
FEATURES

-Built-in 1215K bytes frame buffer. Support up to 864 x 480 at 18bpp display
-Support TFT 18-bit generic RGB and TTL interface panel
-Support 6-bit RGB interface
-Hardware rotation of 0, 90, 180, 270 degree
-Hardware display mirroring
-Hardware windowing
-Programmable brightness, contrast and saturation control
-Dynamic Backlight Control (DBC) via PWM signal
-8/9/16-bit MCU interface
-Built-in clock generator
-Deep sleep mode for power saving
-Operation power voltage:3.3V
-2.54mm pitch through hole
-Dimesions:40.80x69.20x7.60mm

LCD INTERFACE

1,20	GND	Power supply GND
2	VCC	5V,Power Supply(*)
15-18,26	NC	No Connect
3	LD0	Red Data Signals
4	LD2	Red Data Signals
5	LD4	Red Data Signals
6	LD6	Green Data Signals
7	LD8	Green Data Signals
8	LD10	Green Data Signals
9	LD12	Blue Data Signals
10	LD14	Blue Data Signals
11	LD16	Blue Data Signals
12	DEN	Data valid
13	L_LINE	HSYNC,Horizontal sync
14	LD17	Blue Data Signals
19	VCC	3.3V,Power Supply
20	GND	Power supply GND
21	DCLK	TP_chip_DCLK
22	CS	TP_chip_CS
23	DIN	TP_chip_DIN
24	DOUT	TP_chip_DOUT
25	PENIRQ	TP_chip_PENIRQ
27	L_FRAME	VSYNC,Vertical sync
28	SHIFT	PCLK,Pixel clock
29	GAMAS0	Gamma selection for panel
30	LD15	Blue Data Signals
31	LD13	Blue Data Signals
32	LD11	Green Data Signals
33	LD9	Green Data Signals
34	LD7	Green Data Signals
35	LD5	Red Data Signals
36	LD3	Red Data Signals
37	LD1	Red Data Signals
38-39	NC	No Connect
40	PWM	PWM output for backlight driver

(*)5V pin is not connected to DC-DC regulator chip AMS1117 defaultly. Please connect to voltage output pin of AMS1117 if you want to use it.



MPU INTERFACE

1	GND	Power supply GND
2	VCC	3.3V,Power supply
3,16,20	NC	No Connect
4	RS	D/C,Data/Command select
5	WR	6800 mode: R/W# 0: Write cycle 1: Read cycle 8080 mode: WR# (write strobe signal)
6	RD	6800 mode: E (enable signal) 8080 mode: RD# (read strobe signal)
7	D8	16-bit data bus
8	D9	16-bit data bus
9	D10	16-bit data bus
10	D11	16-bit data bus
11	D12	16-bit data bus
12	D13	16-bit data bus
13	D14	16-bit data bus
14	D15	16-bit data bus
15	CS	TP_chip_CS,Chip select
17	RST	Reset,Active LOW Reset signal
18	PWM	PWM output for backlight driver
19	VCC	5V,Power supply(*)
21-26,37	NC	No Connect
27	D7	16-bit data bus
28	D6	16-bit data bus
29	D5	16-bit data bus
30	D4	16-bit data bus
31	D3	16-bit data bus
32	D2	16-bit data bus
33	D1	16-bit data bus
34	D0	16-bit data bus
35	PENIRQ	TP_chip_PENIRQ
36	DOUT	TP_chip_DOUT
38	DIN	TP_chip_DIN
39	CS	TP_chip_CS
40	DCLK	TP_chip_DCLK

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