

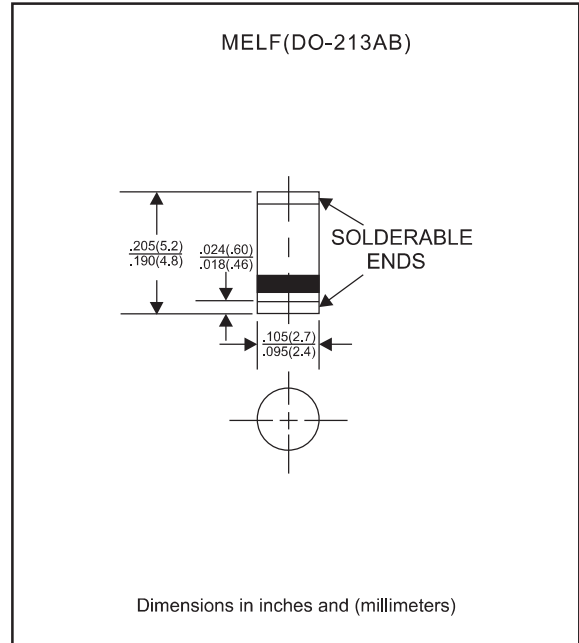
**Features**

- Low profile surface mounted application in order to optimize board space
- High current capability
- High surge capability
- Glass passivated chip junction
- Lead free parts meet RoHS requirements

**Mechanical data**

- Epoxy : UL94-V0 rated flame retardant
- Case : Molded plastic, MELF / DO-213AB
- Terminals :Plated terminals, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any

**Package outline**



**Maximum ratings and Electrical characteristics (AT T<sub>A</sub>=25°C unless otherwise noted)**

PARAMETER	CONDITIONS	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.2	I <sub>O</sub>			1.0	A
Forward surge current	8.3ms single half sine-wave (JEDEC methode)	I <sub>FSM</sub>			30	A
Reverse current	V <sub>R</sub> = V <sub>RRM</sub> T <sub>J</sub> = 25°C	I <sub>R</sub>			5.0	μA
	V <sub>R</sub> = V <sub>RRM</sub> T <sub>J</sub> = 125°C				50	
Thermal resistance	Junction to ambient	R <sub>θJA</sub>		50		°C/W
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C <sub>J</sub>		15		pF
Storage temperature		T <sub>STG</sub>	-65		+175	°C

SYMBOLS	V <sub>RRM</sub> <sup>*1</sup> (V)	V <sub>RMS</sub> <sup>*2</sup> (V)	V <sub>R</sub> <sup>*3</sup> (V)	V <sub>F</sub> <sup>*4</sup> (V)	Operating temperature T <sub>J</sub> , (°C)
DL4001	50	35	50	1.10	-55 to +150
DL4002	100	70	100		
DL4003	200	140	200		
DL4004	400	280	400		
DL4005	600	420	600		
DL4006	800	560	800		
DL4007	1000	700	1000		

\*1 Repetitive peak reverse voltage

\*2 RMS voltage

\*3 Continuous reverse voltage

\*4 Maximum forward voltage@I<sub>F</sub>=1.0A

**Rating and characteristic curves**

FIG.1-TYPICAL FORWARD CHARACTERISTICS

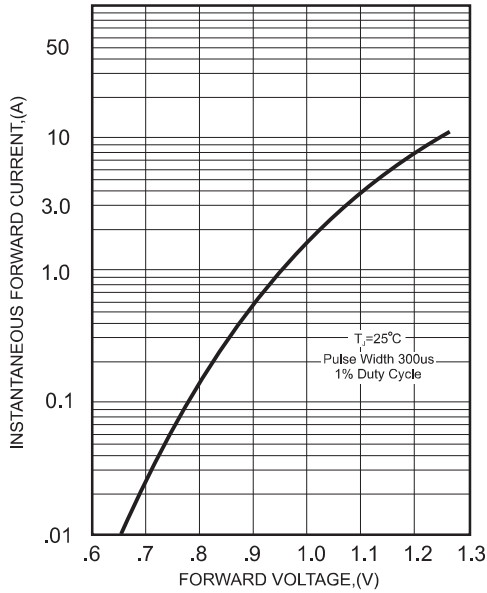


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

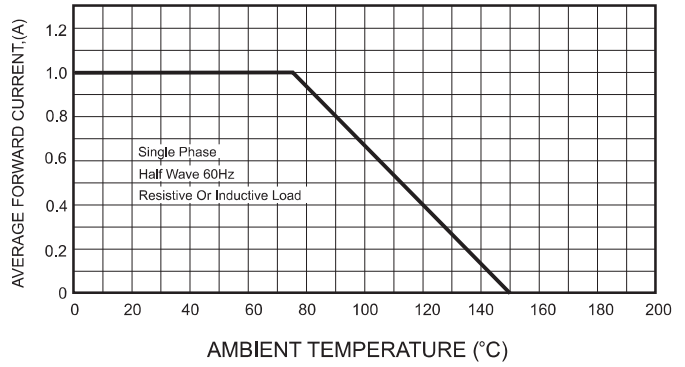


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

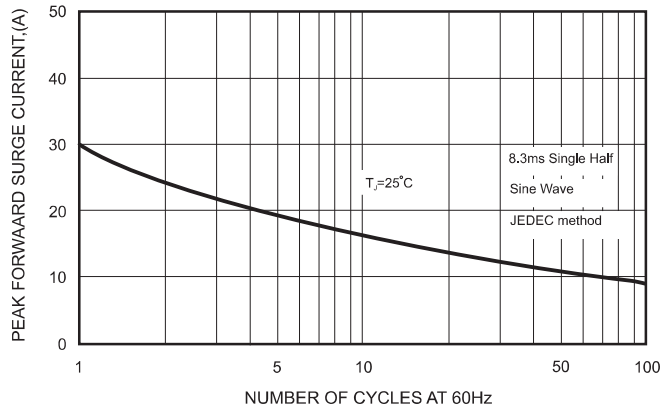


FIG.3 - TYPICAL REVERSE CHARACTERISTICS

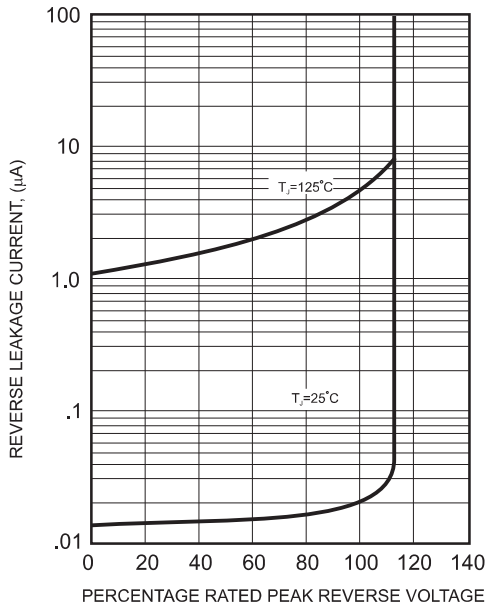
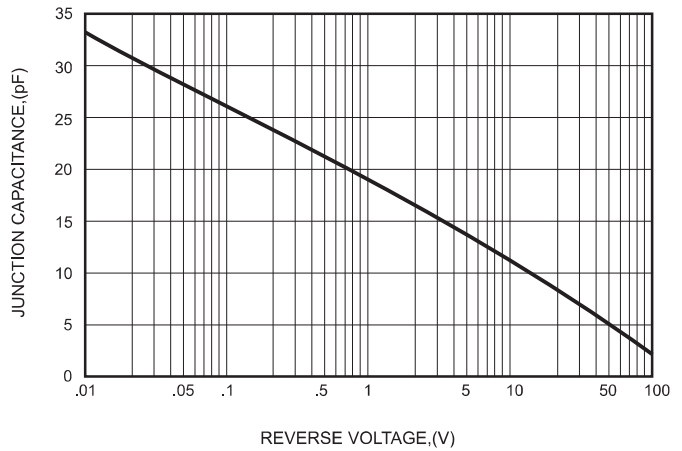




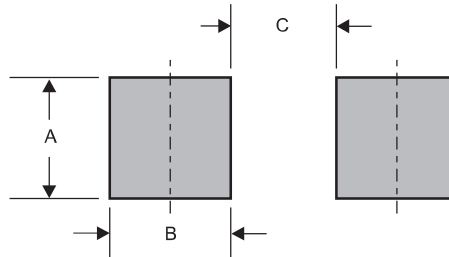
FIG.5-TYPICAL JUNCTION CAPACITANCE



**Pinning information**

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode		

**Suggested solder pad layout**

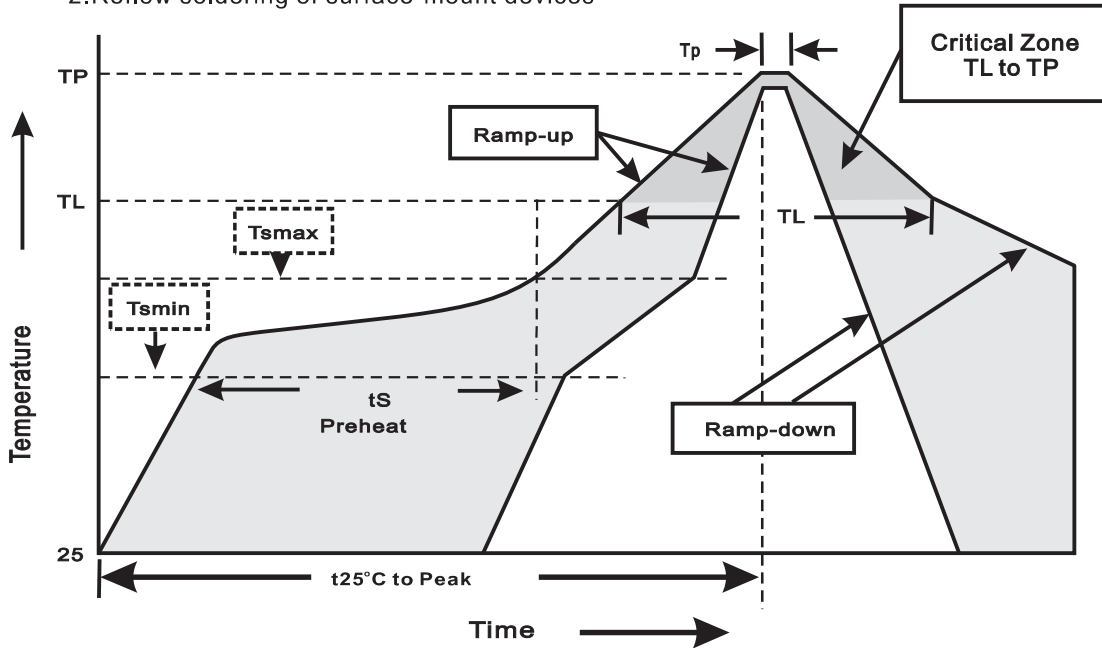


Dimensions in inches and (millimeters)

PACKAGE	A	B	C
MELF	0.118 (3.00)	0.079 (2.00)	0.130 (3.30)

**Suggested thermal profiles for soldering processes**

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
- 2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(TL to TP)	<3°C/sec
Preheat -Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(min to max)(ts)	150°C 200°C 60~120sec
Tsmax to TL -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(TL) -Time(tL)	217°C 60~260sec
Peak Temperature(TP)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(tp)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes